

We Claim:

1. A method for checking the refresh function of a memory having a refresh device, which comprises:

determining if refresh request pulses are being produced on the memory and at what intervals of time the refresh request pulses are being produced on the memory;

supplying a control unit of the memory with refresh test pulses produced outside the memory instead of supplying the control unit with the refresh request pulses; and

checking the refresh device of the memory utilizing the refresh test pulses.

2. The method according to claim 1, which further comprises controlling the checking of the refresh device with the control unit.

3. The method according to claim 1, which further comprises supplying the refresh test pulses and the refresh request pulses to a multiplex device.

4. The method according to claim 2, which further comprises supplying the refresh test pulses and the refresh request pulses to a multiplex device.

5. The method according to claim 1, which further comprises supplying the refresh test pulses and the refresh request pulses to the control unit through a multiplex device.

6. The method according to claim 2, which further comprises supplying the refresh test pulses and the refresh request pulses to the control unit through a multiplex device.

7. The method according to claim 1, which further comprises:

connecting a multiplex device to the control unit; and

supplying the refresh test pulses and the refresh request pulses to the control unit through the multiplex device.

8. The method according to claim 2, which further comprises:

connecting a multiplex device to the control unit; and

supplying the refresh test pulses and the refresh request pulses to the control unit through the multiplex device.

9. The method according to claim 3, which further comprises controlling the multiplex device with a test signal.

10. The method according to claim 4, which further comprises controlling the multiplex device with a test signal.
11. The method according to claim 5, which further comprises controlling the multiplex device with a test signal.
12. The method according to claim 6, which further comprises controlling the multiplex device with a test signal.
13. The method according to claim 7, which further comprises controlling the multiplex device with a test signal.
14. The method according to claim 8, which further comprises controlling the multiplex device with a test signal.